

# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

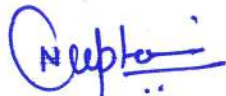
## B.Tech in Electronics and Communication with Specialization in IOT

VI - SEMESTER(w.e.f. Batch 2018-19)

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTECIOT601	Application Development for IOT		3	0	2	60	20	20	30	20	150	4
2	BTEC603	Discrete Time Signal Processing		3	1	2	60	20	20	30	20	150	5
3	BTECIOT602	Data Analytics for IOT		3	0	2	60	20	20	30	20	150	4
4	BTECIOT603	IOT Architecture and Protocols		3	1	0	60	20	20	0	0	100	4
5	BTEC525/ BTEI604/ BTEC714	Elective: 1 FPGA Based System Design 2 Digital Image Processing 3 Embedded Systems for Robotics		3	1	2	60	20	20	30	20	150	5
6	BTECIOT604	Embedded System with Raspberry pi and Arduino		0	0	2	0	0	0	30	20	50	1
7	BTEC507	Programming in Python		0	0	2	0	0	0	30	20	50	1
<b>TOTAL</b>				<b>15</b>	<b>3</b>	<b>12</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>180</b>	<b>120</b>	<b>800</b>	<b>24</b>

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

\* Students are advised to take online MOOCS course of minimum two credits



**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore



**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, INDORE



**Controller of Examinations**  
Shri Vaishnav Vidyapeeth Vishwavidyalaya



**Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore



**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore